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BIBDATASHEET

CONFIRMATION NO. 5822

Bib Data Sheet

SERIAL NUMBER 09/986,583	FILING DATE 11/09/2001 RULE	CLASS 714	GROUP ART UNIT 2133	ATTORNEY DOCKET NO. 57454-280
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APPLICANTS

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** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****

JAPAN 2001-172187(P) 06/07/2001 *DT*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 11/27/2001

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 24	TOTAL CLAIMS 28	INDEPENDENT CLAIMS 4
Verified and Acknowledged	<i>David Tan</i> Examiner's Signature	<i>DT</i> Initials			

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TITLE

Semiconductor integrated circuit device capable of self-testing internal power supply currents provided to internal circuits integrated on chip

FILING FEE RECEIVED 968	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____
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